

**ABSTRACT OF THE DISCLOSURE**

A multi-layered packet processing device using multiple processors for processing a received multi-layered packet at a hardware level. The multi-layered packet processing device includes an interface for transmitting a data packet to  
5 a node and receiving the data packet from the node through a public network; and a plurality of packet processing portions for sequentially processing the data packet, in a pipeline pattern, according to a header of the data packet transferred through the interface.

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